

DMG48270C043_04WN

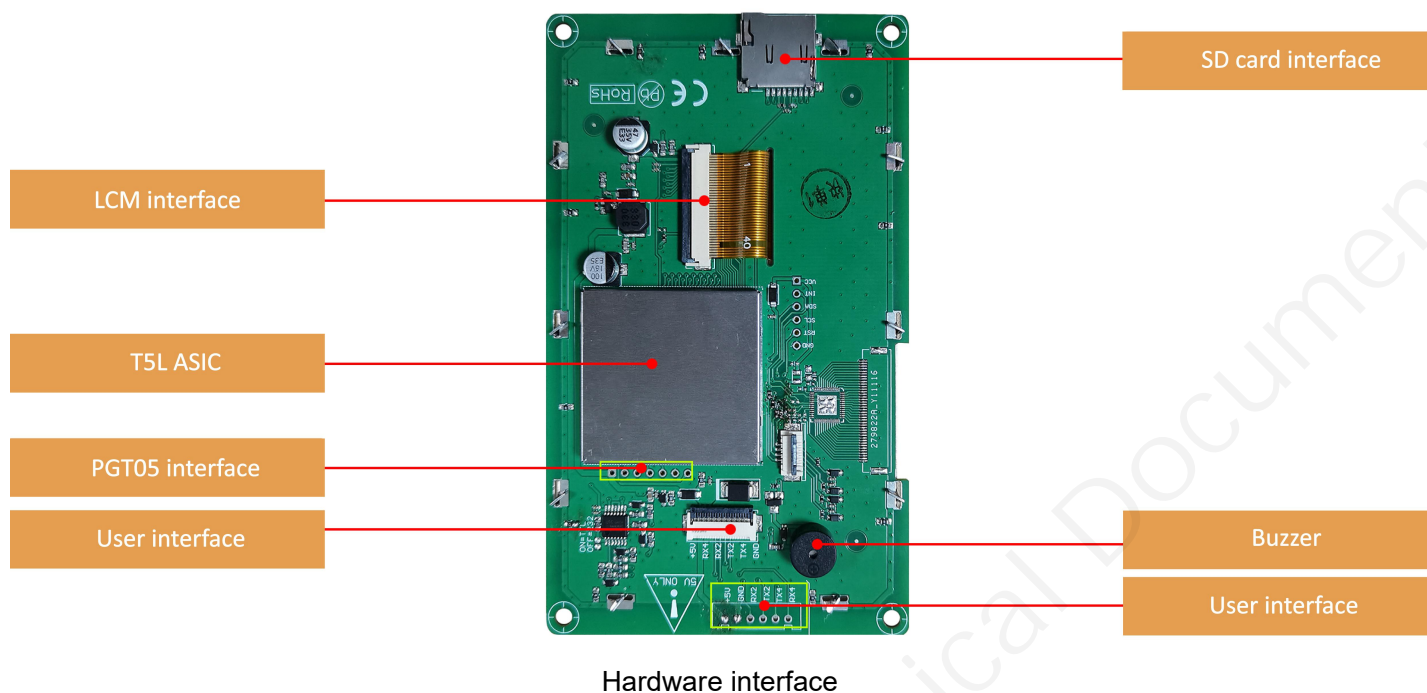
Features:

- Based on T5L0, running DGUS II system, commercial grade.
- 4.3-inch, 480*272 Pixels resolution, 262K Colors, TV-TN-TFT-LCD.



1. Hardware and interface

1.1 Hardware interface



1.2 Hardware and interface description

No.	Name	Description
1	T5L0 ASIC	Developed by DWIN. Mass production in 2020, 1MBytes Nor Flash on the chip, 512KBytes used to store the user database. Rewrite cycle: over 100,000 times
2	LCM interface	FPC40_0.5mm, RGB interface
3	User interface	10Pin_1.0mm latching socket for power supply and serial communication. Download rate(typical value): 12KByte/s
4	Flash	16MBytes NOR Flash, for fonts, pictures and audio files. Rewrite cycle: over 100,000 times
5	Buzzer	3V passive buzzer. Power: <1W
6	SD card interface	FAT32. Download files by SD interface can be displayed in statistics. Download rate: 4Mb/s
7	PGT05 interface	When product crashes by accident, you can use PGT05 to update DGUS kernel and make the product return to normal

2. Specification parameters

2.1 Display parameters

LCD Type	TV-TN, TFT LCD
Viewing Angle	TV viewing angle, 70°/70°/40°/30° (L/R/U/D)
Resolution	480×272 pixels (0°/90°/180°/270°)
Color	18-bit 6R6G6B
Active Area (A.A.)	95.04mm (W)×53.86mm (H)
View Area (V.A.)	-
Backlight Mode	LED
Backlight Service Life	>20000 hours (Time of the brightness decaying to 50% on the condition of continuous working with the maximum brightness)
Brightness	300nit
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)
Note: You can use dynamic screen saver wallpapers to avoid afterimages caused by fixed page display for a long time.	

2.2 Serial interface parameters

Mode	UART2: ON=TTL/CMOS; OFF=RS232 UART4: ON=TTL/CMOS; OFF=RS232 (Only available after OS configuration)				
Voltage Level	Test Condition	Min	Typ	Max	Unit
	Output 1, Iout = -4mA	2.7	3.2	-	V
	Output 0, Iout = 4mA	-	0.1	0.4	V
	Input 1	2.4	3.3	5.5	V
	Input 0	0	-	1.0	V
Baud Rate	3150~3225600bps, typical value of 115200bps				
Data Format	UART2: N81 UART4: N81/E81/O81/N82 , 4 modes (OS configuration)				
Interface Cable	10Pin_1.0mm				

2.3 Electrical specifications

Rated Power	<5W	
Operating Voltage	4.5~5.5V, typical value of 5V	
Operating Current	200mA	VCC=5V, max backlight
	70mA	VCC=5V, backlight off
Recommended power supply: 5V 1A DC		

2.4 Operating environment

Operating Temperature	-10℃~60℃ (5V @ 60% RH)
Storage Temperature	-20℃~70℃
Conformal Coating	None
Operating Humidity	10%~90%RH, typical value of 60% RH

3. Reliability test

3.1 Electrostatic discharge test

☒ Test standard : ☐ EN 61000-4-2:2009 ☒ IEC 61000-4-2:2008 ☐ GB/T 17626.2-2018
☐ Other:

Table 1: Electrostatic Discharge Immunity (Air Discharge)

Test Points Locations	Test Levels							
	-2kV	+2kV	-4kV	+4kV	-8kV	+8kV	-15kV	+15kV
Screen					A	A		
/	/	/	/	/	/	/	/	/
/	/	/	/	/	/	/	/	/

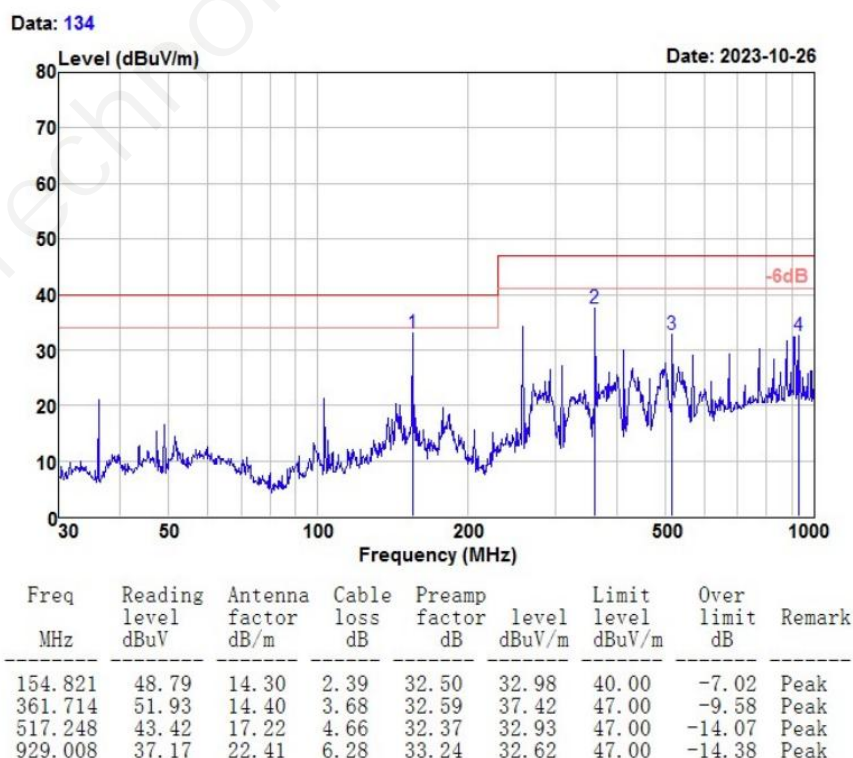
Table 2: Electrostatic Discharge Immunity (Direct Contact)

Test Points Locations	Test Levels							
	-2kV	+2kV	-4kV	+4kV	-6kV	+6kV	-8kV	+8kV
Frame					B	B		
/	/	/	/	/	/	/	/	/
/	/	/	/	/	/	/	/	/

3.2 RE test

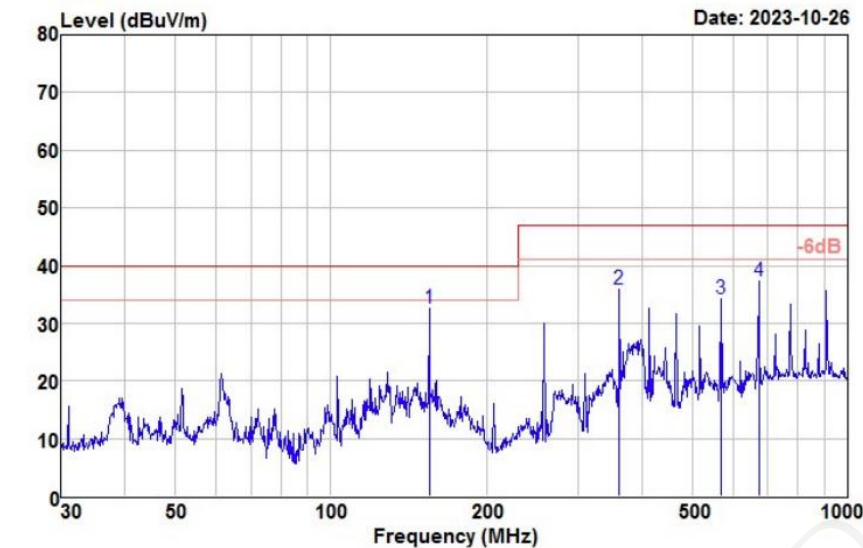
Test Item	Test Standard	Result
RE	ClassA-6dB	Normal operation

HORIZONTAL



VERTICAL

Data: 133



Freq MHz	Reading level dBuV	Antenna factor dB/m	Cable loss dB	Preamp factor dB	level dBuV/m	Limit level dBuV/m	Over limit dB	Remark
154.821	48.42	14.30	2.39	32.50	32.61	40.00	-7.39	Peak
361.714	50.50	14.40	3.68	32.59	35.99	47.00	-11.01	Peak
568.613	43.82	18.06	4.88	32.57	34.19	47.00	-12.81	Peak
672.845	45.13	19.46	5.17	32.41	37.35	47.00	-9.65	Peak

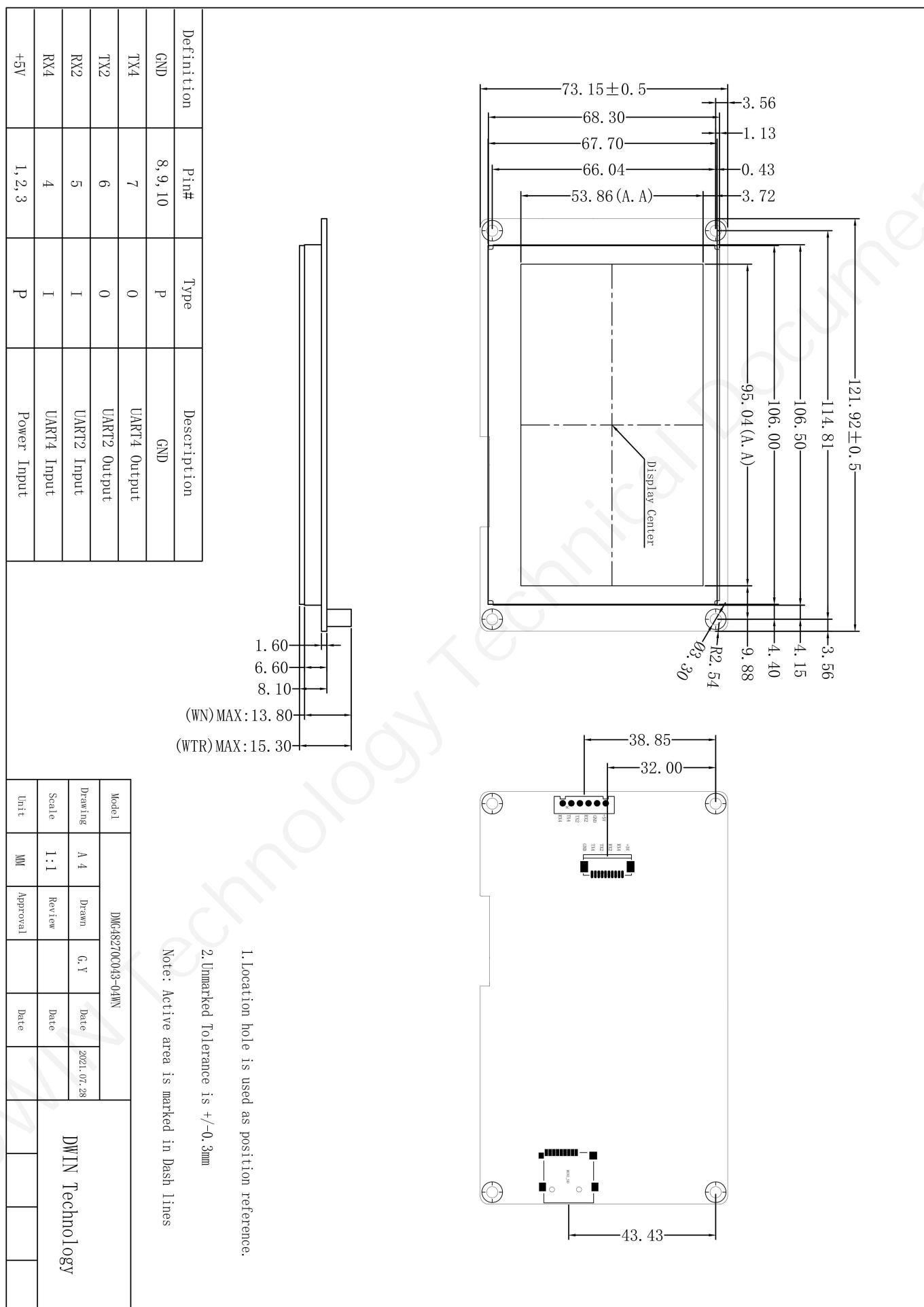
Performance Criterion:

- A. Normal performance within limits specified by the manufacturer, requestor or purchaser;
- B. Temporary loss of function or degradation of performance which ceases after the disturbance ceases, and from which the equipment under test recovers its normal performance, without operator intervention;
- C. Temporary loss of function or degradation of performance, the correction of which requires operator intervention;
- D. Loss of function or degradation of performance which is not recoverable, due to damage to hardware or software, or loss of data.

4. Packaging & dimensions

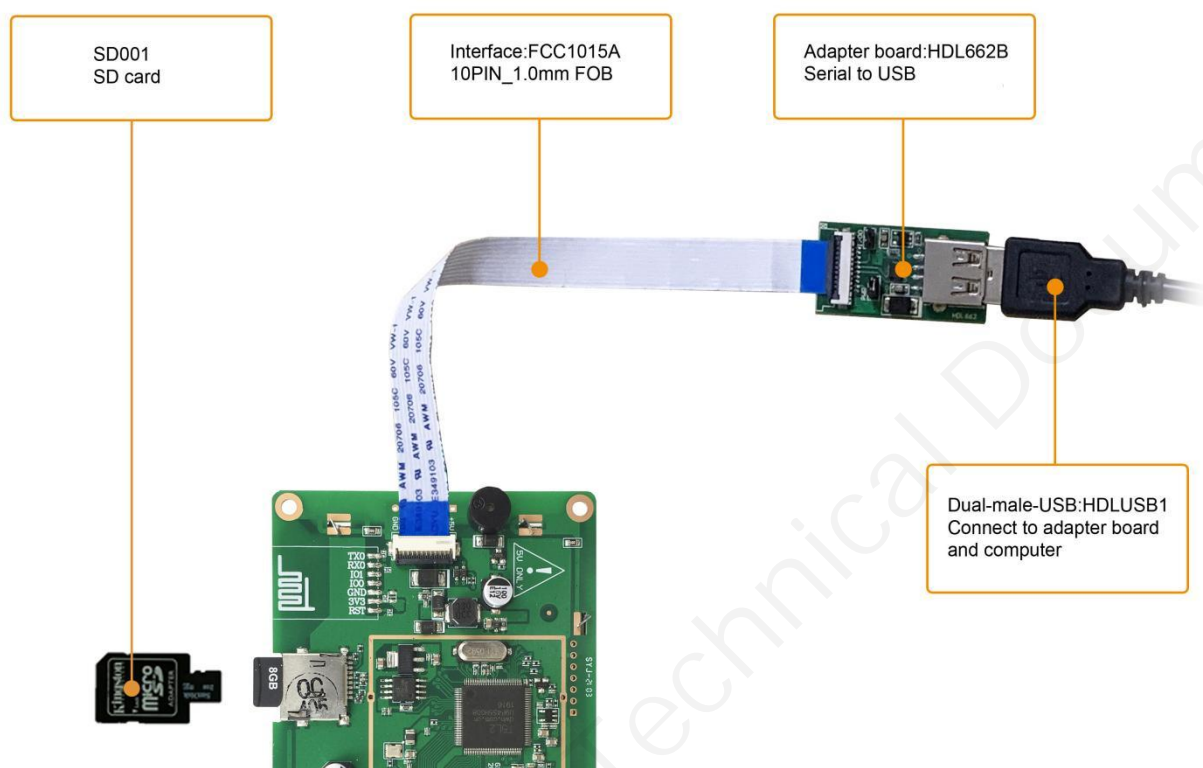
Form Factor	121.92mm (W)× 73.15mm (H)× 13.80mm (T)			
Installation Dimensions	Positioning hole: 106.50(+0.3mm)× 68.30(+0.3mm)			
Net Weight	105g			
Packaging Standards				
Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)
Carton1:	220mm(L)× 160mm(W)× 47mm (H)	1	2	2
Carton2:	250mm(L)× 200mm(W)× 80mm (H)	2	2	4
Carton3:	320mm(L)× 270mm(W)× 80mm (H)	2	4	8
Carton4:	450mm(L)× 350mm(W)× 300mm(H)	2	30	60
Carton5:	600mm(L)× 450mm(W)× 300mm(H)	2	60	120

Disclaimer: The product design is subject to alternation and improvement without prior notice.



5. Debugging tools

It is recommended for new users of DWIN smart LCMs to purchase official accessories. For more details, please refer to customer service center.



6. T5L series IC features

- (1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle)high speed operation.
- (2) Separate GUI CPU Core running DGUS II System:
 - High-speed display memory, 2.4GB/S bandwidth.
 - 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
 - Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
 - Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
 - 1-way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
 - 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
 - Support DGUS development and simulation on PC. Support background remote upgrade.
- (3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:
 - Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
 - 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
 - 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channle 16-bit PWM of adjustable resolution.
 - Support IAP on-line simulation and debugging with unlimited number of breakpoints.
 - Upgrade code online through DGUS system.
- (4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- (5) Operating temperature ranges from -40°C to +85°C(IC operating temperature customizable from -55°C to 105°C).

DWIN encourages users to design your own customized product based on T5L

7. Revision records

Rev	Revise Date	Content	Editor
00	2023-11-29	First Edition	Xu Ying

Please contact us if you have any questions about the use of this document or our products, or if you would like to know the latest information about our products:

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!